

SynTest TurboBIST-Logic™ Tutorial

1. TurboBIST-Logic Capabilities

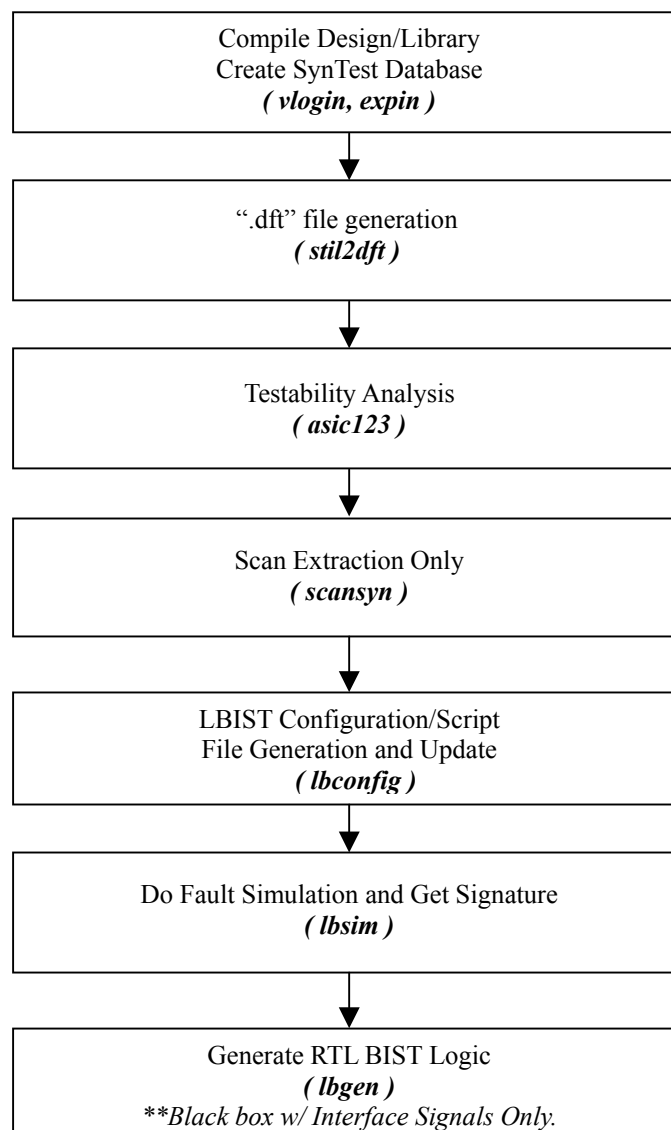
SynTest's TurboBIST-Logic is a powerful and easy-to-use tool to create Logic Built-In Self-Test (LBIST) circuits for embedded cores or Intellectual Property's (IPs).

TurboBIST-Logic has the capabilities to:

- Perform testability analysis on a scan-ready core for conformance to logic BIST design rules.
- Incorporate logic BIST circuitry to the scan-ready core for at-speed logic BIST capability.
- Compute logic BIST fault coverage for the core-under-test.

Note: In this demonstration package, the netlist to TurboBIST-Logic is scan inserted and only interface signals of the logic BIST controller are given.

2. Tool Flow



3. TurboBIST-Logic for Simple Design(s)

Here we use simple design(s) for this tutorial. First time users should familiar themselves with the Readme, the run script, and all the prepared files under the directories before invoking the run script. At the end of the run, users should examine tool-produced intermediate and output files to gain an understanding of the flow, operation and DFT technology. Advanced users can use `--help` in each software module to see other options available in the demo software.