Errata for *Embedded Systems: ARM Programming and Optimization*

Updated on February 4, 2016

Errata received and confirmed as of February 4, 2016 are shown below. Please submit any errata not listed here to n.mcfadden@elsevier.com

---

- Chapter 1, Section 1.6.2, Page 18

Change: `gcc bubble_sort_asm.s -o bubble_sort -O3`

To: `gcc bubble_sort_asm.s -o bubble_sort_asm -O3`

- Chapter 1, Section 1.6.3, Page 19

Change: "main:"

To: "bubble_sort_asm:"

- Chapter 1, Section 1.9.3, Page 34

The `cnts_close` function should be moved before the `cnts_tock` function, or should be declared at the beginning of the file.

- Chapter 1, Section 1.10, Page 35

Change: `#define SIZE    128<<20`

To: `#define N       128<<20`

The code uses `N` vs `SIZE`.

Change: `#elif A15`
             `#define CLOCK_RATE    2230.5e6`

To: `#elif A15`
             `#define CLOCK_RATE    2320.5e6`

▪ Chapter 1, Exercise 1.3, Page 46

Change: `bge`

To: `ble`

▪ Chapter 2, Exercise 2.1, Page 102

Should refer to "Section 1.6", not "Section 2.1"

▪ Chapter 3, Exercise 3.9, Page 143

Exercise 3.9 and 3.10 were merged into one exercise. Exercise 3.10 begins with "Section 3.6.5..."

▪ Chapter 3, Exercise 3.10, Page 144

Exercises 3.10 and 3.11 were merged into one exercise. Exercise 3.11 begins with "In this question we examine..."

▪ Chapter 3, Exercise 3.11b (3.13b with corrected sequence), Page 144

The second sentence of exercise 3.11b (3.13b) should read: "The macro can perform the 64-bit add using two 32-bit adds and use the carry flag to signal carries from the least significant 32-bit group to the most significant 32-bit group."

▪ Chapter 5, Table 5.1, Page 210

Row 3, Column 1: "*shared*" should be "*local*"