

Computer Organization and Design: The Hardware/Software Interface
 David A. Patterson and John L. Hennessy
 Errata list as of 10/09/12

Chapter	Page #	Description	Correction	Notes																		
Green Card		There is a small mistake in the "Green MIPS Reference Card" inserted in the front. The mistake is that the symbols for the logical and arithmetic right shifts ('>>' and '>>>') are interchanged.	Specifically, the line (in the left column of the front of the card): Shift Right Logical <code>srl R R[rd] = R[rt] >> shamt</code> should read: Shift Right Logical <code>srl R R[rd] = R[rt] >>> shamt</code> And the line (in the right column of the front of the card): Shift Right Arith. <code>sra R R[rd] = R[rt] >>> shamt</code> should read: Shift Right Arith. <code>sra R R[rd] = R[rt] >> shamt</code>																			
1	42	Figure 1.16: A logarithmic scale has no 0	The lowest label on the y-axis should be 1 rather than 0																			
1		Kindle Edition only, Exercise 1.8: Data table for Exercise 1.8 is incorrect	Table should read: <table border="1" style="margin-left: 20px;"> <thead> <tr> <th></th> <th>Version</th> <th>Voltage</th> <th>Clock Rate</th> </tr> </thead> <tbody> <tr> <td rowspan="2">a.</td> <td>Version 1</td> <td>1.75 V</td> <td>1.5 GHz</td> </tr> <tr> <td>Version 2</td> <td>1.2 V</td> <td>2 GHz</td> </tr> <tr> <td rowspan="2">b.</td> <td>Version 1</td> <td>1.1 V</td> <td>3 GHz</td> </tr> <tr> <td>Version 2</td> <td>0.8 V</td> <td>4 GHz</td> </tr> </tbody> </table>		Version	Voltage	Clock Rate	a.	Version 1	1.75 V	1.5 GHz	Version 2	1.2 V	2 GHz	b.	Version 1	1.1 V	3 GHz	Version 2	0.8 V	4 GHz	
	Version	Voltage	Clock Rate																			
a.	Version 1	1.75 V	1.5 GHz																			
	Version 2	1.2 V	2 GHz																			
b.	Version 1	1.1 V	3 GHz																			
	Version 2	0.8 V	4 GHz																			
2	182	In the second table on page 182 the code for table row (a.) is incorrect.	<code>slll</code> instruction should be <code>sll</code>																			
2	182	In the second table on page 182 the code for table row (b.) is incorrect.	The code for (b.) should read: <pre>sll \$t0, \$s0, 2 # \$t0 = f * 4 add \$t0, \$s6, \$t0 # \$t0 = 6A[f] sll \$t1, \$s1, 2 # \$t1 = g * 4 add \$t1, \$s7, \$t1 # \$t1 = 6B[g] lw \$s0, 0(\$t0) # f = A[f] addi \$t2, \$t0, 4 lw \$t0, 0(\$t2) add \$t0, \$t0, \$s0 sw \$t0, 0(\$t1)</pre>																			
5	464	Example, middle of page: 1200/6=75 should be 1200/16=75	Change 1200/6 to 1200/16																			
7	676	Missing closing parentheses in Figure 7.17, part c.	The "Crossbar Switching..." lines should have closing parentheses																			
Solutions Manual		Solutions Manual: Errors have been found in the solutions to: 2.2.3 (b) 2.3.3 (b) 2.4.1 (b) 2.4.4 (b) 2.4.5 (a and b) 2.6.1 (a and b) 2.6.2 (a and b) 2.6.3 (a and b) 2.6.4 (a and b) 2.6.5 (a and b)	Instructors can download the corrected solutions file from: http://textbooks.elsevier.com/web/manuals.aspx?isbn=9780123747501 (requires one-time registration)																			

Solutions Manual		Solutions Manual: Errors has been found in the solution to 6.15.1(a)	The correct answer is 8AEC (not DEE8)	
---------------------	--	---	---------------------------------------	--