

Chapter-4

The Route to Market for Silicon Photonics

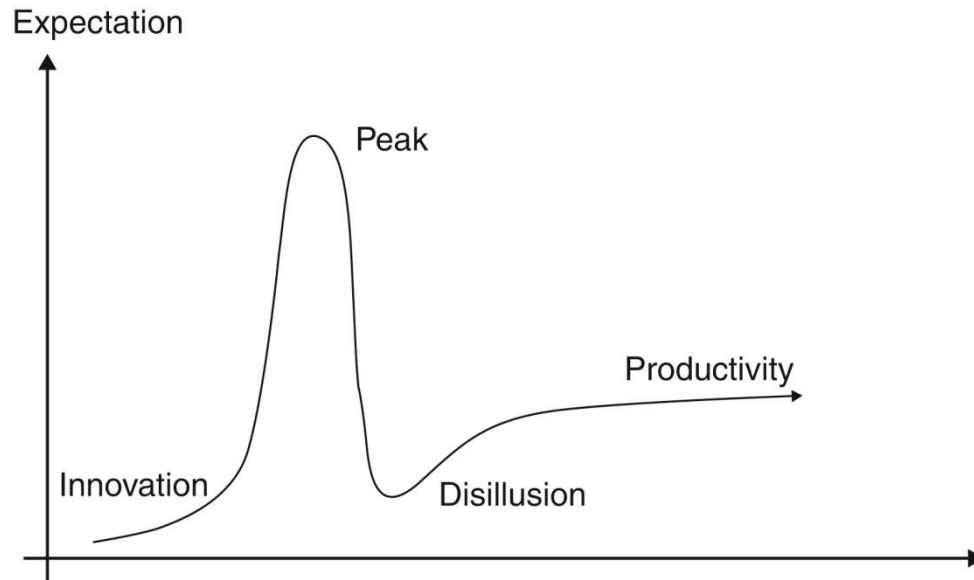


Figure 4.1: Gartner's four-stage technology development cycle. Based on Gartner.

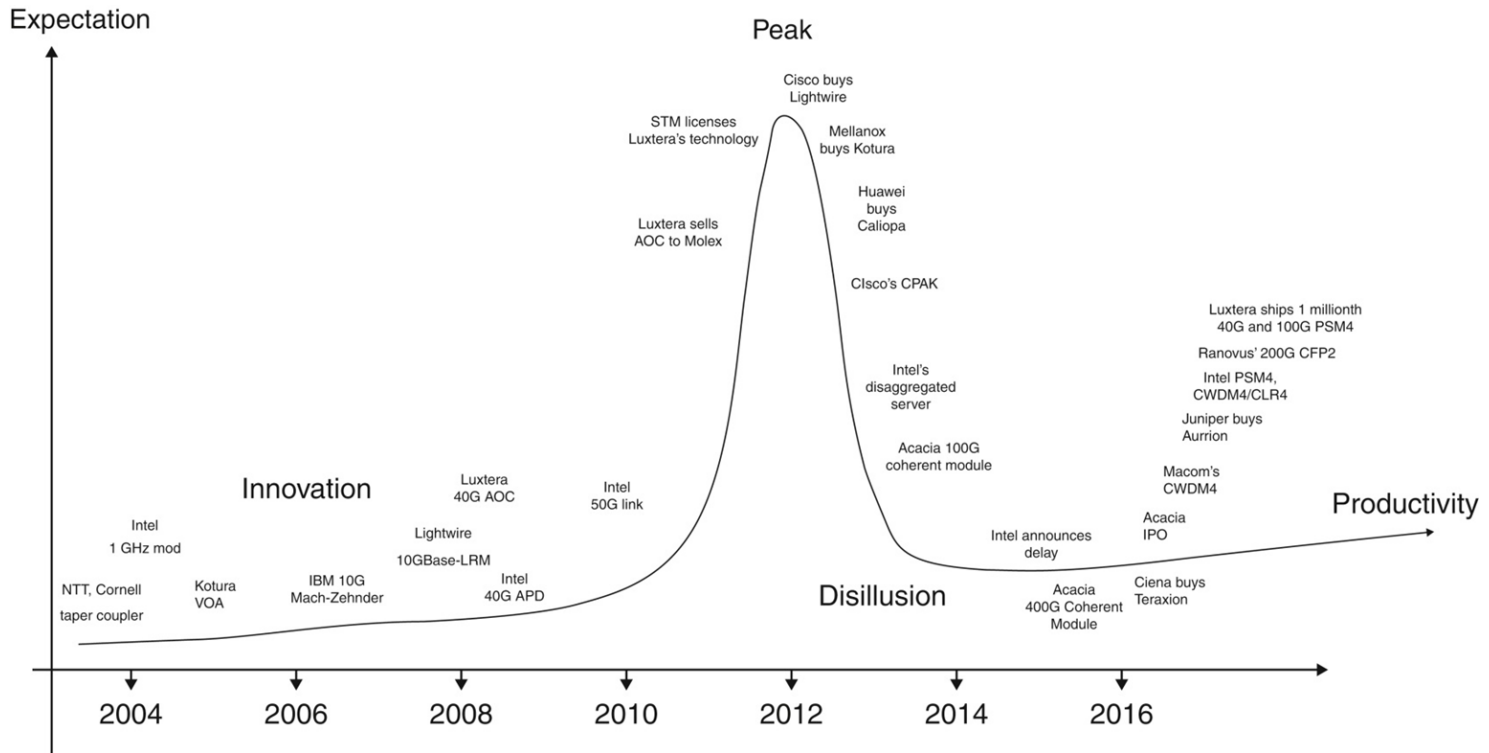


Figure 4.2: Silicon photonics' technology development cycle.



Figure 4.3: Mellanox's (Kotura's) variable optical attenuator. Courtesy of Mellanox Technologies.

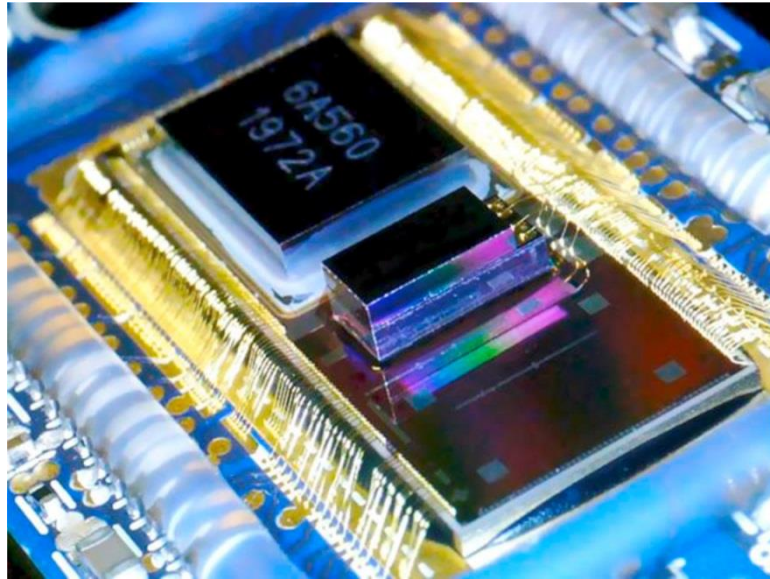


Figure 4.4: Luxtera's 100-Gb PSM4 silicon photonics chip. From Luxtera.



Figure 4.5: Cisco's CPAK. From Cisco.

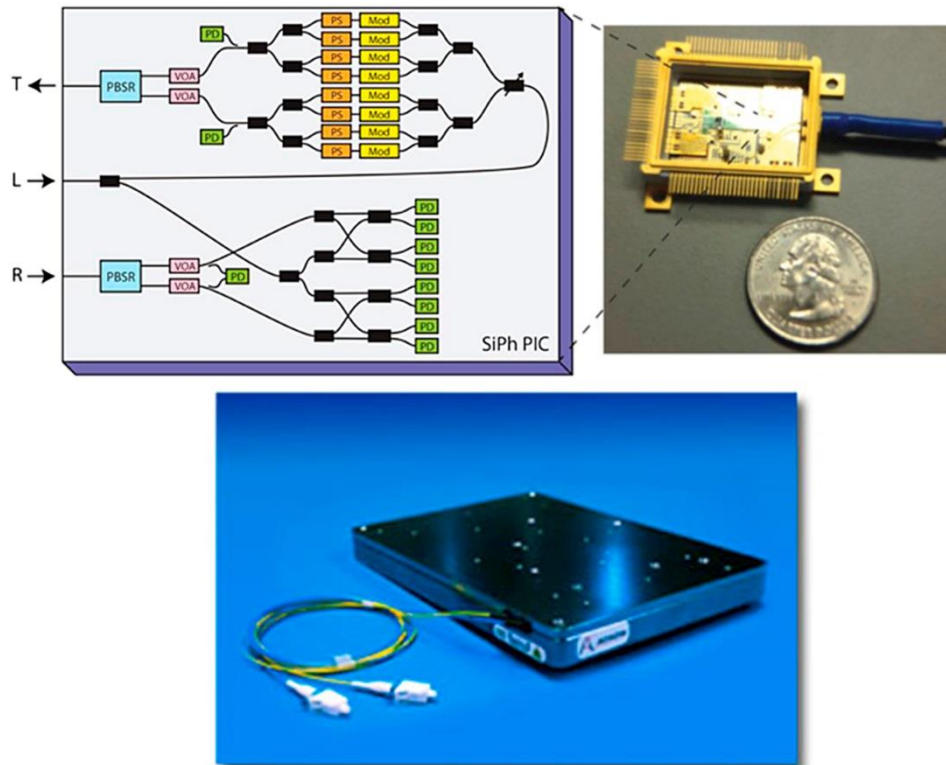


Figure 4.6: Acacia Communications' coherent transceiver. From Acacia Communications.

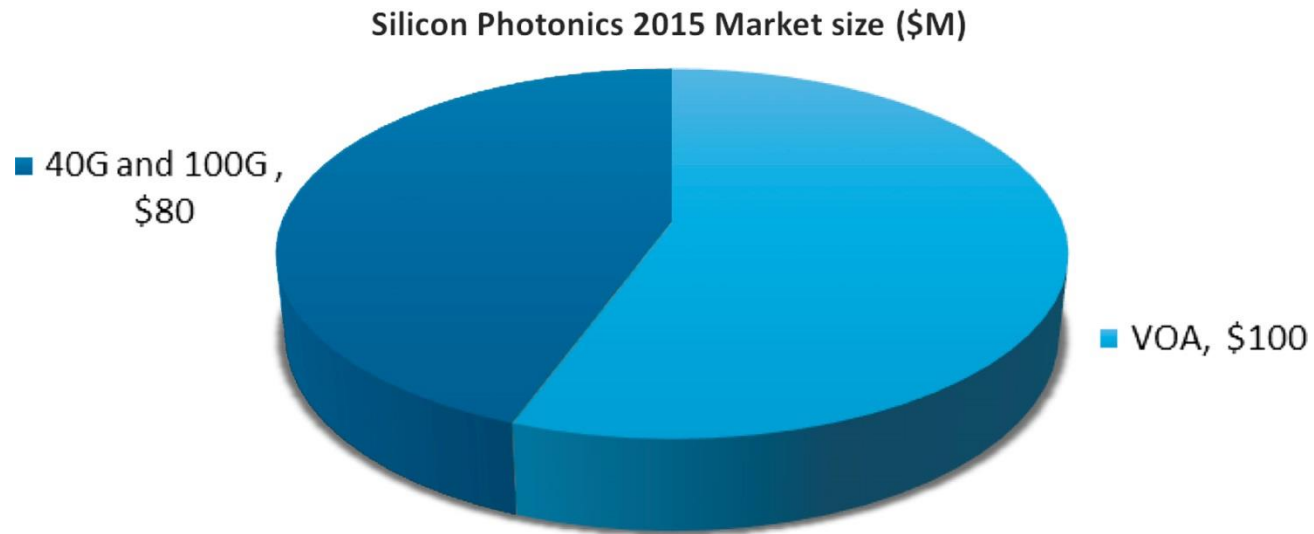


Figure 4.7: Revenues in 2015 from silicon photonics product sales.

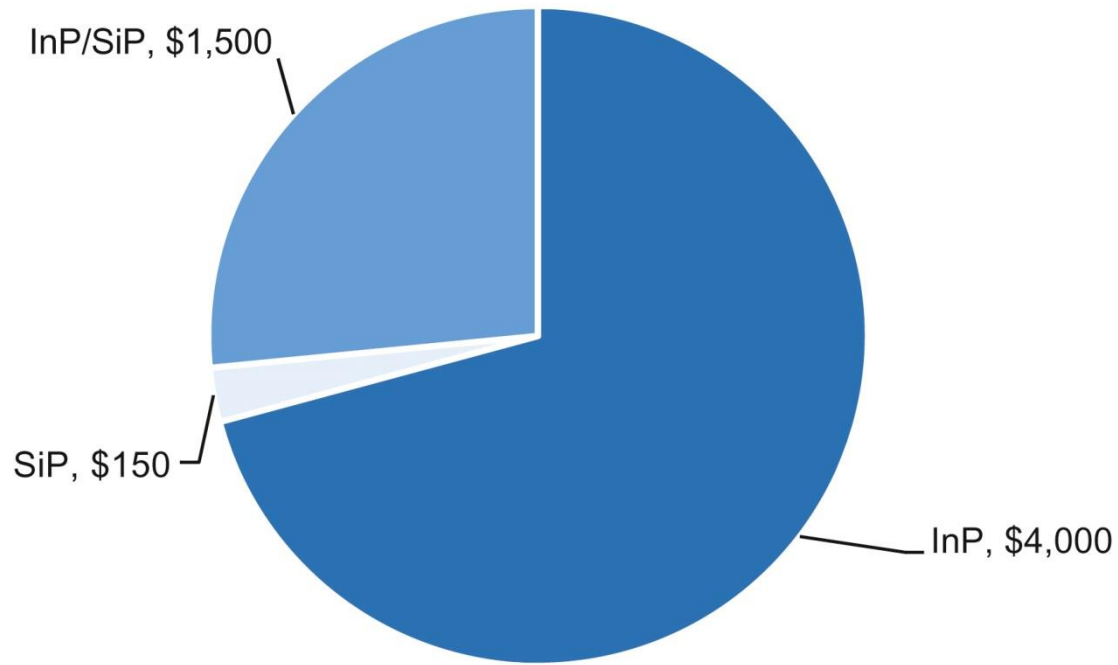


Figure 4.8: 100-Gb single-mode aggregate transceiver revenues by technology for 2016-2020 (\$ millions).









	Shipping	Developing	Potential users	
2015				
2016				

Figure 4.9: Vendors participating in silicon photonics' commercialization.