Chapter 6

Input and Output

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isotropic

nematic

Figure 6.1: Liquid crystal states.



Figure 6.2: Orientation of a liquid crystal to an electric field.



Figure 6.3: LCD architecture.



Figure 6.4: Passive and active matrix liquid crystal displays.



Figure 6.5: A compensation circuit for an OLED [Ono07]..



Figure 6.6: Operation of a DLP cell.



Figure 6.7: An inkjet nozzle.



Figure 6.8: Cross section of a pixel.



Figure 6.9: A filter array and the Bayer pattern..



Figure 6.10: Operation of a three-phase CCD.



Figure 6.11: Schematic of an active pixel sensor (APS) cell.



Figure 6.12: Architecture of a CMOS image sensor.



Figure 6.13: Circuit for capacitive sensing.



Figure 6.14: Architecture of a resistive touch screen.



Figure 6.15: An inertial sensor.